



# CUSTOMER ADVISORY

## ADV1821

### Ordering Part Number and Software Update for Intel® Programmable Acceleration Card with Intel Arria®10 GX FPGA

#### Change Description:

Intel Programmable Solutions Group ("Intel PSG", formerly Altera) is notifying customers of the following changes on the Intel® Programmable Acceleration Card (PAC) with Intel® Arria 10 GX FPGA:

Table 1

	Change From	Change To	Details
<b>Ordering Part Number (OPN)</b>	BD-ACD-10AX1152A	BD-ACD-10AX1152B	<ul style="list-style-type: none"><li>• New OPN for the changes listed below</li></ul>
<b>FPGA Interface Manager (FIM) in the FPGA flash</b>	Supports Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.1	Supports Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.2	<ul style="list-style-type: none"><li>• Includes a service (daemon) which monitors sensor thresholds</li><li>• Updated QSFP features allow default transceiver setting for Intel cables to be included in FIM</li><li>• Supports package of OPAE for Ubuntu 16.04</li><li>• OPAE tools have been extended to read temperature data from the FIM</li></ul>
<b>Board Management Controller (BMC)</b>	26822	26889	<ul style="list-style-type: none"><li>• BMC is updated to support PLDM 1.1.1</li><li>• BMC current threshold has been updated so that the 12V rail does not draw more than 5.5A+9% per the PCIe specification. Prior threshold was 6.67A</li><li>• Added MCTP Commands for SMBus features</li><li>• Includes remote update capability over PCIe when used Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.2 or later</li></ul>
<b>USB Cable</b>	Included	None	<ul style="list-style-type: none"><li>• USB no longer required</li></ul>

## Recommended Action:

Customers are advised to take note of the changes to the PAC and of the following:

1. For Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.1 and BMC earlier than rev. 26889, the very first flash of rev. 26889 firmware needs to still be performed over USB. Subsequent BMC firmware flashing operations can then be performed over PCIe.
2. After the last order date, customers can no longer order the BD-ACD-10AX1152A OPN and must instead order BD-ACD-10AX1152B. Customers should move to the new OPN by February 28, 2019.
3. Intel recommends that all customers move to the latest Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.2 on existing Intel® Programmable Acceleration Card (PAC) with Intel® Arria 10 GX FPGA.
4. The TA and AA numbers can be located on the box and board respectively.

**Table 2**

OPN	Rev.	AA Number	TA Number	Last Time Buy	Last Time Ship
BD-ACD-10AX1152A	1.1	K24559-001	K10085-001	November 30, 2018	February 28, 2019

OPN	Rev.	AA Number	TA Number	First Time Order	First Ship Date
BD-ACD-10AX1152B	1.2	K38015-001	K38019-001	October 30, 2018	January 30, 2019

## Products Affected:

**Table 3**

Ordering Part Number
BD-ACD-10AX1152A

## Reason for Change:

The updates to the FIM and BMC allow for improved monitoring capabilities, better support connectivity, and enhanced acceleration stack capabilities.

## Change Implementation:

Table 4

<b>Milestone</b>	<b>Date</b>
Release date of Intel® Acceleration Stack for Xeon® CPUs with FPGAs version 1.2 and BMC 26889	November 5, 2018

## Contact

For more information, please submit a Service Request at the [My Intel](#) support page.

## Customer Notifications Subscription

Customers that have subscribed to Intel PSG's customer notification mailing list will receive the ADV document automatically via email.

If you would like to receive customer notifications by email, please subscribe to our customer notification mailing list at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mal-emailsub/technical-updates.html>

## Revision History

<b>Date</b>	<b>Rev</b>	<b>Description</b>
10/15/2018	1.0.0	Initial Release

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